

**Search Notes**

Application/Control No.

10/612,189

Examiner

Peter Coughlan

Applicant(s)/Patent under  
Reexamination

WEDLAKE ET AL.

Art Unit

2129

**SEARCHED**

Class	Subclass	Date	Examiner
706	1	1/12/2006	PDC
706	15	1/12/2006	PDC
706	45	1/12/2006	PDC
700	1	1/12/2006	PDC
700	90	1/12/2006	PDC
717	1	1/12/2006	PDC
340	825.31	1/12/2006	PDC
714	38	1/12/2006	PDC
273	84	1/12/2006	PDC
714	6	1/12/2006	PDC
711	151	1/12/2006	PDC
340	635	1/12/2006	PDC
356	419	1/12/2006	PDC

**INTERFERENCE SEARCHED**

Class	Subclass	Date	Examiner

**SEARCH NOTES  
(INCLUDING SEARCH STRATEGY)**

	DATE	EXMR
East-template portion, issue, error, code portion, format, status, fan, power supply, analyzing the output, primary secondary	1/12/2006	PDC
East--cont--boot, primary and secondary kernel, tamper, corrupt, database, log, pattern, path, logical, critical running,	1/12/2006	PDC
East--contl--hardware, software, system dump, debug, enable, preform, detect, predict, crash, dynamic error, circular buffer,	1/12/2006	PDC
East, full disk, daemons, master software, file system,	1/12/2006	PDC
Inventors--William P Wedlake, Michael Joseph Brandon, Peter Van Giel, Jean Gluannotte, Peter Put	1/12/2006	PDC
717.1 with code template & error out 340.825.31 with log, flag, software switch, corruption, analyzing diagnostic log	1/12/2006	PDC
714.38 with I/O status, predict, crash, dump, 273.84 with dynamic error and threshold, kernels	1/12/2006	PDC
714.6 with hardware and software, boot, paths, file system	1/12/2006	PDC